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May 30, 2003

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TECHNOLOGY CENTER 2800

Christopher W. Lattin  
United States Patent and Trademark Office  
Crystal Plaza 4, 3B28  
Arlington, VA 22202

Re: Pending United States Patent Application Serial No. 09/884,172  
"Method of Fabricating CMOS Inverter and Integrated Circuits Utilizing Strained  
Silicon Surface Channel MOSFETs" by Fitzgerald *et al.*  
Old File No.: 5994B  
Our Docket No. ASC-044 (366/P12580)

Dear Mr. Lattin:

In accordance with our conversation of May 29, 2003, enclosed is a copy of the Supplemental Information Disclosure Statement and Form 1449 submitted April 17, 2003 and, for your convenience, replacement copies of the non-U.S. patent references and other publications. Also enclosed is a copy of the Supplemental Information Disclosure Statement and Form 1449 submitted May 21, 2003. The references corresponding to this Supplemental IDS were also submitted May 21, 2003.

Please acknowledge receipt of these documents by returning the enclosed postcard. Please contact us if you have any questions.

Very truly yours,



Natasha C. Us

Enclosures

cc: Steven J. Frank, Esq. (w/o encls.)  
Mark L. Beloborodov (w/o encl.)